

AMENDMENTS TO THE CLAIMS

Claims 1-6. (Cancelled)

7. (Previously Presented) A system comprising:

a remote host comprising a wireless circuit for communicating digital data; and

at least two programmable logic devices connected in a circuit, each of said at least two programmable logic devices comprising:

a wireless transceiver for receiving said digital data from said remote host;

a base band unit connected to said wireless transceiver to perform data processing operations on said digital data; and

a programmable logic component connected to said base band unit using said digital data to configure said programmable logic component; and

wherein each of said at least two programmable logic devices does not have a wired connection to a read-only memory for use in initial configuration.

8. (Original) The system of claim 7 wherein said wireless transceiver of at least one of said at least two programmable logic devices is connected to an amplifier.

9. (Original) The system of claim 7 wherein said host and said at least two programmable logic devices conform to Bluetooth protocol.

10. (Original) The system of claim 7 wherein said at least two programmable logic devices are FPGAs and said digital data is configuration bitstream data.

11. (Original) The integrated circuit of claim 7 wherein at least one of said at least two programmable logic devices transmits a reply to said remote host.

12. (Original) The system of claim 7 wherein said digital data is generated by an external source, and wherein said wireless circuit further comprises:

an interface for receiving said digital data from said external source;

a processor for processing said digital data; and

a transceiver for transmitting said digital data to said at least two programmable logic devices.

13. (Original) The system of claim 12 wherein said wireless transceiver of at least one of said at least two programmable logic devices is connected to an amplifier.

14. (Original) The system of claim 12 wherein said at least two programmable logic devices and said host conform to Bluetooth protocol.

15. (Original) The system of claim 12 wherein said at least two programmable logic devices are FPGAs and said digital data is configuration bitstream data.

16. (Original) The integrated circuit of claim 12 wherein at least one of said at least two programmable logic devices transmits a reply to said remote host.

17. (Original) The system of claim 7 wherein said at least two programmable logic devices have different start times, and wherein said host transmits start commands to said at least two programmable logic devices at different times.

18. (Original) The system of claim 17 wherein said at least two programmable logic devices and said host conform to Bluetooth protocol.

19. (Original) The system of claim 17 wherein said at least two programmable logic devices are FPGAs and said digital data is configuration bitstream data.

20. (Original) The integrated circuit of claim 17 wherein at least one of said at least two programmable logic devices transmits a reply to said remote host.

21. (Original) The system of claim 7 further comprising at least one slave programmable logic device, wherein at least one of said at least two programmable logic devices is a master device delivering said digital data to said at least one slave programmable logic device.

22. (Original) The system of claim 21 wherein said at least two programmable logic devices and said host conform to Bluetooth protocol.

23. (Original) The system of claim 21 wherein said at least two programmable logic devices are FPGAs and said digital data is configuration bitstream data.

24. (Original) The integrated circuit of claim 21 wherein at least one of said at least two programmable logic devices transmits a reply to said remote host.

Claims 25-45. (Cancelled)